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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki      Art Unit : 2811  
Serial No.: 09/774,388      Examiner : Thien F. Tran  
Filed : January 30, 2001      Confirmation No. : 3058  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD  
THEREOF

Commissioner for Patents  
Washington, D.C. 20231

AMENDMENT

In response to the Official Action dated October 21, 2002,  
Paper No. 10, in the above-referenced application, kindly send  
the above-identified application as follows.

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JAN 30 2003  
TECHNOLOGY CENTER 2800

In the Abstract:

Please replace the Abstract of the Disclosure with the  
following rewritten Abstract as follows:

B  
A high performance electric device which uses an adhesive  
layer over a substrate. A color filter is over a substrate, and  
an adhesive layer is also located over the substrate and color  
film. An insulating layer is over the adhesive layer, and thin  
film transistors cover the insulating film and the color  
filters. Light emitting elements cover the thin film

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being  
deposited with the United States Postal Service as first class mail with  
sufficient postage on the date indicated below and is addressed to the  
Commissioner for Patents, Washington, D.C. 20231.

01/29/2003 GGEBREGI 00000046 09774388

01 FC:1202      162.00 OP  
02 FC:1201      252.00 OP

January 21, 2003

Date of Deposit

Signature

*Susan Regan*

Susan Regan

Typed or Printed Name of Person Signing Certificate